

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Simon Deleonibus CONFIRMATION NO.: 8398
SERIAL NO.: 10/539,928
FILING DATE: 05/02/2006
TITLE: MIS TRANSISTOR WITH SELF-ALIGNED GATE AND METHOD
FOR MAKING SAME
EXAMINER: Salerno, Sarah Kate
ART UNIT: 2814

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the Restriction Requirement mailed August 19, 2008, Applicants hereby elect Group 1, Claims 1-7 without traverse. Applicants reserve the right to pursue the non-elected claims in a divisional/continuation application. Early favorable consideration is earnestly solicited.

Please charge any additional required fee or credit any overpayment not otherwise paid or credited to our deposit account no. 50-1698.

Respectfully submitted,
THELEN LLP

Dated: 09/18/2008

/Khaled Shami/
Khaled Shami
Reg. No. 38,745

THELEN LLP
P.O. Box 640640
San Jose, CA 95164-0640
Tel. (408) 292-5800
Fax. (408) 287-8040